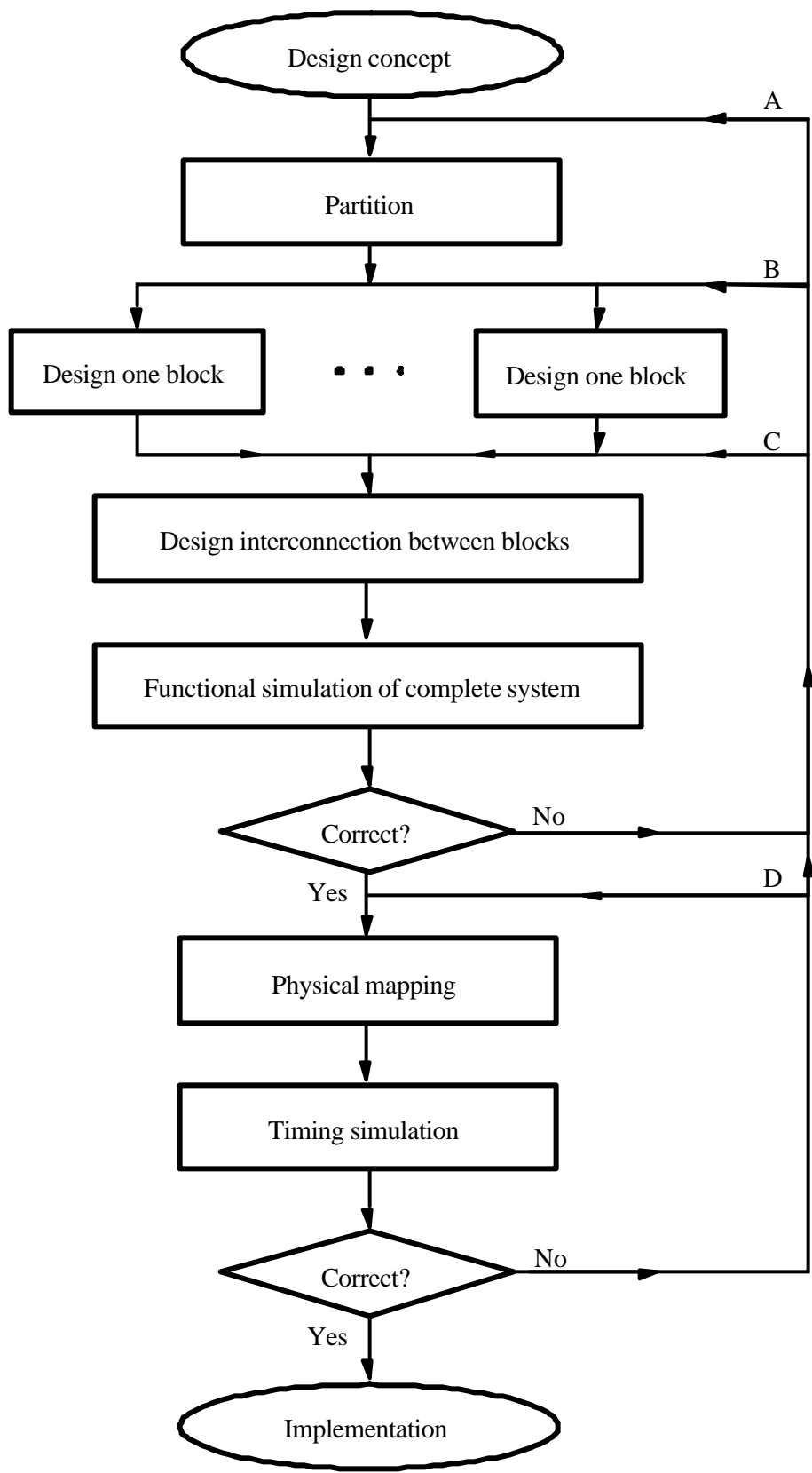Figure 1.3   The development process

Figure 1.6 Design flow for logic circuits